

Electronic Acknowledgement Receipt

EFS ID:	1227907
Application Number:	10604491
Confirmation Number:	1490
Title of Invention:	Multiprocessor subsystem in SoC with bridge between processor clusters interconnection and SoC system bus
First Named Inventor:	Christos J. Georgiou
Customer Number:	23389
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Filer Authorized By:	Steven Fischman
Attorney Docket Number:	FIS920030113
Receipt Date:	29-SEP-2006
Filing Date:	25-JUL-2003
Time Stamp:	15:28:31
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$ 790
RAM confirmation Number	720
Deposit Account	090458
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Request for Continued Examination (RCE)	FIS920030113US1_RCE.pdf	112787	no	2
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed	FIS920030113US1_IDS.pdf	3325380	no	5
Warnings:					
Information:					
3	NPL Documents	Almasi.pdf	639895	no	17
Warnings:					
Information:					
4	NPL Documents	Georgiou.pdf	512967	no	9
Warnings:					
Information:					
5	NPL Documents	IBMIntroducesPowerPC.pdf	65857	no	2
Warnings:					
Information:					
6	NPL Documents	CoreConnectBusArchitecture.pdf	76881	no	2
Warnings:					
Information:					
7	NPL Documents	ARMPProcessorCoresOverview.pdf	114203	no	2
Warnings:					
Information:					
8	NPL Documents	BrocadeMultiprotocolRouting Services.pdf	1323412	no	14
Warnings:					
Information:					
9	Fee Worksheet (PTO-875)	fee-info.pdf	8207	no	2
Warnings:					

Information:	
Total Files Size (in bytes):	6179589
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